

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of:	)	
Jonathon C. STIFF, et al.	)	Examiner: William HERNANDEZ
	)	
Application No.: 10/796,859	)	Group Art Unit: 2816
	)	
Filed: March 8, 2004	)	Confirmation No.: 2439
	)	
For: CIRCUIT AND METHOD FOR	)	
IMPLEMENTING A LOW SUPPLY	)	Date: May 21, 2008
VOLTAGE CURRENT REFERENCE	)	

**SUBMISSION OF FORMAL DRAWINGS**

Mail Stop ISSUE FEE  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Applicants hereby submit two (2) sheets of formal drawings for Figures 1-4 for review by the Patent Office in connection with the above-identified application, each sheet marked "REPLACEMENT SHEET." Should the enclosed drawings require changes, it is respectfully requested that the Patent Office notify the undersigned of same.

Customer No.: 60909

Should the Patent Office have any questions regarding this submission or the application in general, the Patent Office is urged to contact the Applicants' attorney, Andrew J. Bateman, by telephone at (408) 943-6878. All correspondence should continue to be directed to the address given below.

Respectfully submitted,

By: Andrew J. Bateman  
Andrew J. Bateman  
Attorney for Applicants  
Registration No. 45,573

Cypress Semiconductor Corporation  
198 Champion Court  
San Jose, CA 95134  
Facsimile: (408) 545-6911  
Customer No.: 60909

Customer No.: 60909